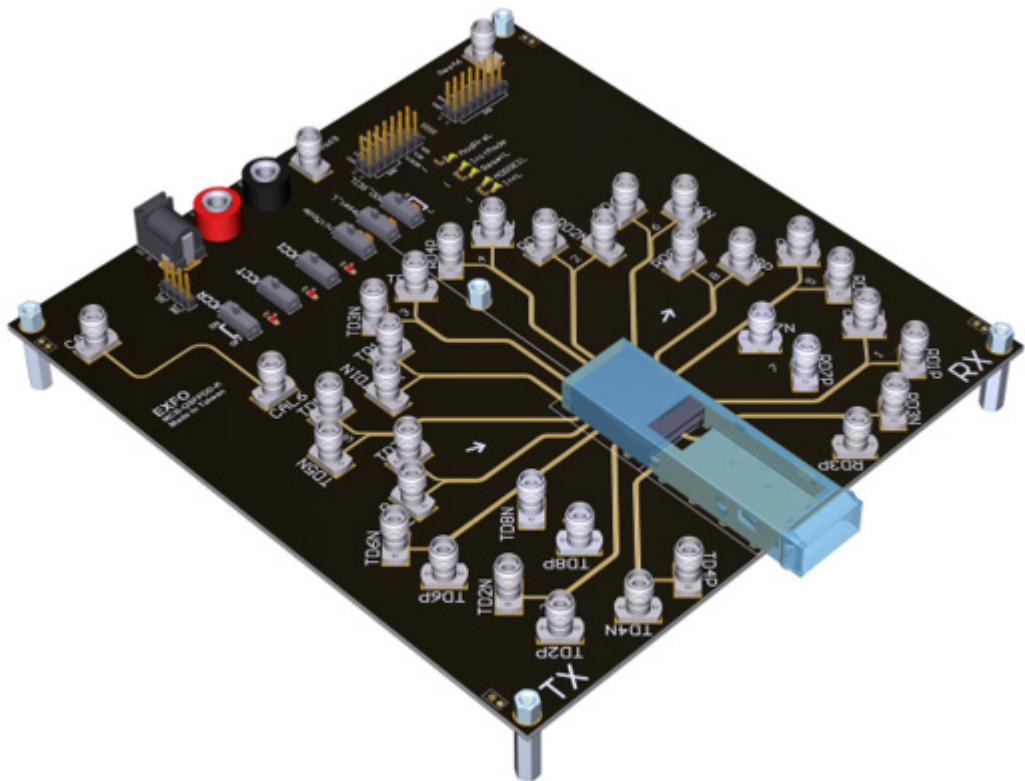


# MCB

## Module Compliance Board



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The exhaustive list of patents is available at [EXFO.com/patent](http://EXFO.com/patent).

Version number: 1.0.0.1

# Contents

<b>1 MCB Models .....</b>	<b>1</b>
<b>2 QSFPDD-O .....</b>	<b>3</b>
I <sup>2</sup> C Pin Set Configuration .....	3
Pin .....	5
Module Edge Connector and Cage .....	8
Schematic of QSFP-DD MCB (O-SMPM) .....	9
<b>3 OSFP-O .....</b>	<b>11</b>
I <sup>2</sup> C Pin Set Configuration .....	11
Pin .....	12
Module Edge Connector and Cage .....	17
Schematic of OSFP MCB (O-SMPM) .....	18
<b>4 QSFPDD-K .....</b>	<b>21</b>
Introduction .....	21
Reference Schematic .....	23
<b>5 QSFP28-K .....</b>	<b>25</b>
Hardware Diagram .....	25
Reference Schematic .....	26
<b>6 SFP28-K .....</b>	<b>27</b>
SFP28-K-90 .....	27
SFP28-K-180 .....	27
Reference Schematic .....	28
<b>7 Low Speed Electrical Hardware Signals .....</b>	<b>29</b>
QSFPDD-K Examples .....	29
Module Edge Connector and Cage .....	35
<b>A QSFPDD-K Examples .....</b>	<b>37</b>
Low Power Mode .....	37
High Power Mode .....	39
<b>Index .....</b>	<b>41</b>



# 1 MCB Models

The MCB (module compliance board) tests next generation transceivers by enabling serial communication between the BER (bit error rate) tester and the optical transceiver. The following table lists and describes all of the MCB models.

MCB Model	Form Factor	RF Connector	Size (W x D) (mm)	Operation Temperature (°C)	Max. Current (mA)	Connector Durability (Typical Cycle)
QSFPDD-O	QSFP-DD	1 x 8 O-SMPM	127 x 114	-20 to 85	6000	100
QSFPDD-K	QSFP-DD	K (2.92 mm)	170 x 160	-40 to 85	6000	100
OSFP-O	OSFP	1 x 8 O-SMPM	127 x 127	-20 to 85	6000	100
OSFP-K	OSFP	K (2.92 mm)	170 x 160	-40 to 85	6000	100
QSFP28-K	QSFP28	K (2.92 mm)	130 x 100	-40 to 85	2500	250
SFPDD	SFP-DD	K (2.92 mm)	160 x 90	-40 to 85	2500	100
DSFP	DSFP	K (2.92 mm)	160 x 90	-40 to 85	6000	100
QSFP56-O	QSFP56	1 x 8 O-SMPM	124 x 114	-20 to 85	2500	100
SFP28-90	SFP28	90° K (2.92 mm)	100 x 65	-40 to 85	2500	250
SFP28-180	SFP28	180° K (2.92 mm)	100 x 65	-40 to 85	2500	250

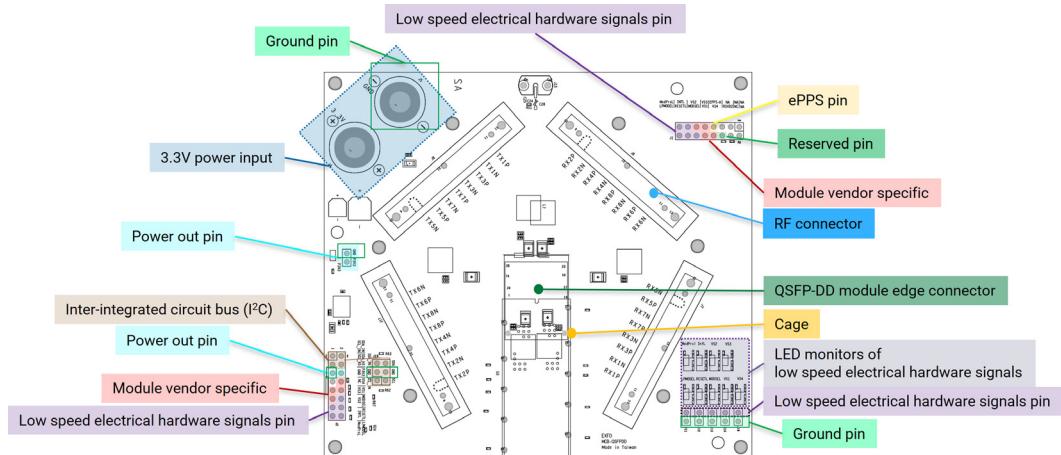
All models support the following:

- VNA test ports
- Inter-Integrated Circuit Bus (I<sup>2</sup>C)
- Standard MSA low speed electrical hardware signals
- Input Voltage of 3.1 V to 3.5 V, typically 3.3 V

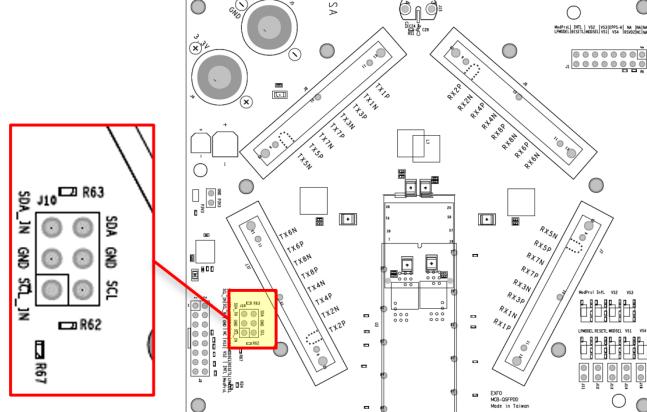
It is recommended to use external max. current slow acting fuse to protect the board in case the power supply has current limit issues.



# 2 QSFPDD-O



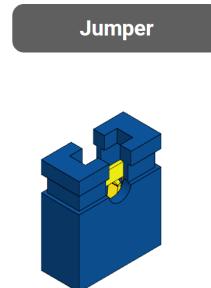
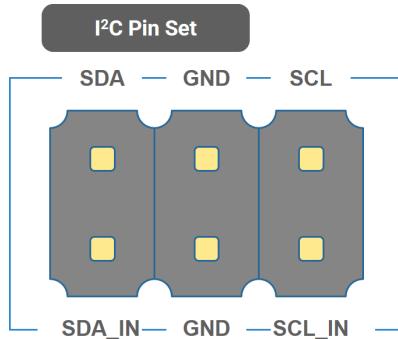
## I<sup>2</sup>C Pin Set Configuration



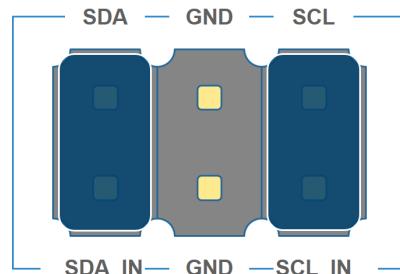
On the Module Test Board, there is an I<sup>2</sup>C pin set.

**To access I<sup>2</sup>C signal by the EXFO USB-I<sup>2</sup>C Controller:**

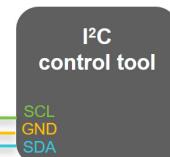
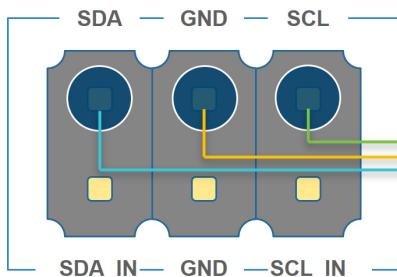
Using the I<sup>2</sup>C pin set and 2 jumpers,



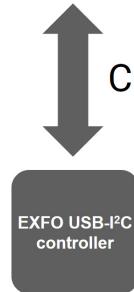
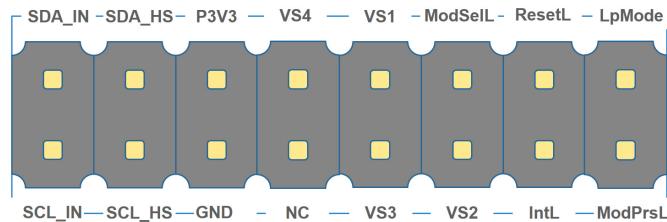
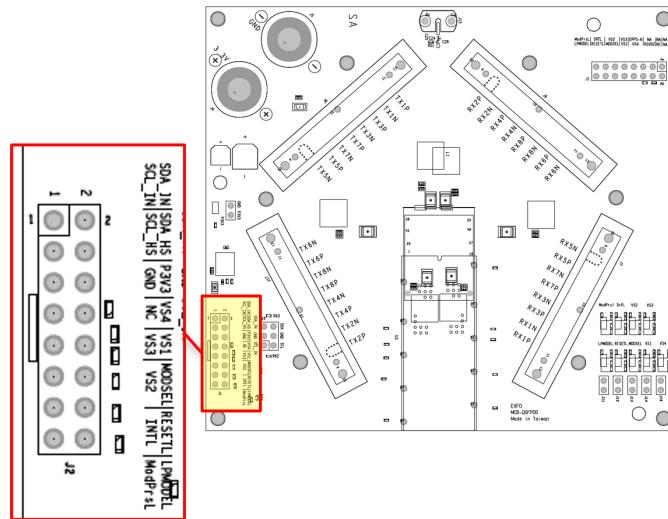
connect 2 jumpers joining SDA to SDA\_IN and SCL to SCL\_IN.

**To access I<sup>2</sup>C signal by an external tool:**

Remove the above jumpers and connect SDA and SCL to the tool.

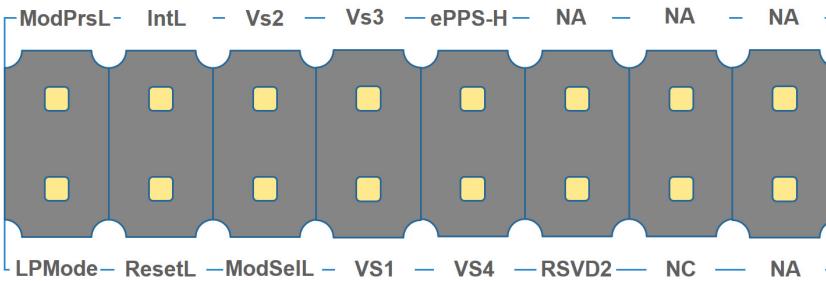
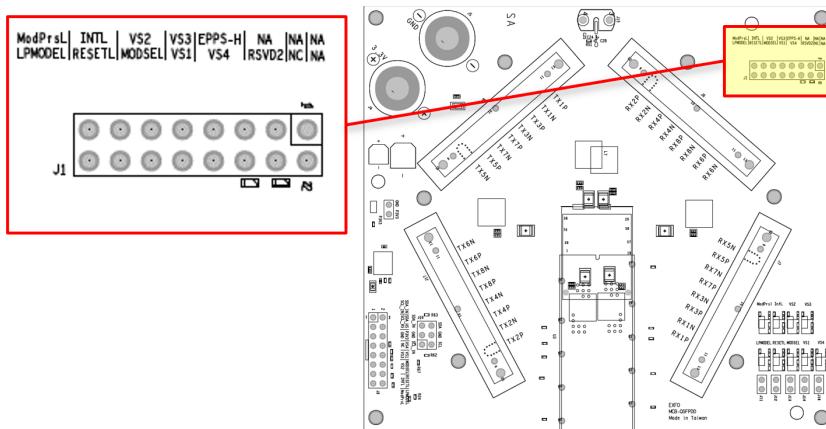


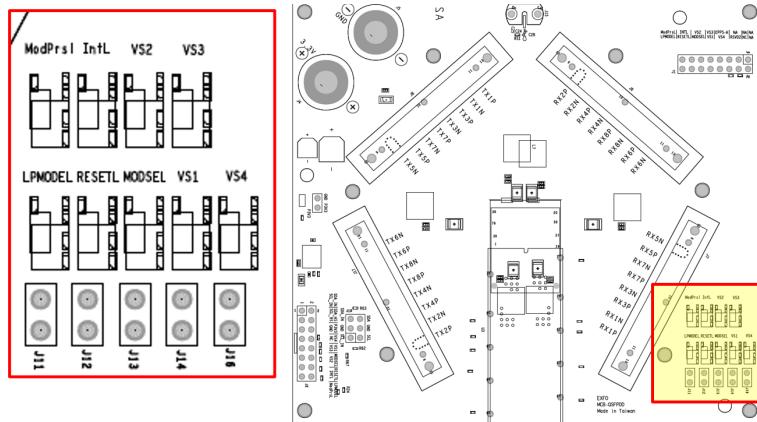
## Pin



# QSFPDD-O

## Pin





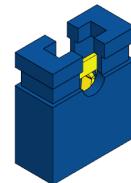
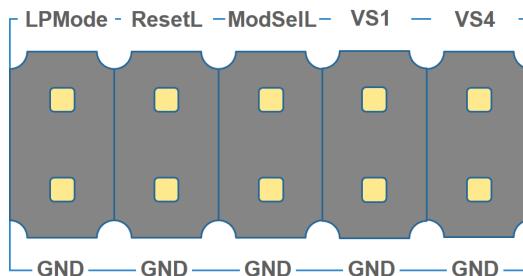
LED On = High, LED Off = Low

All LEDs will turn on when the board power is on without TRx and any jumper.

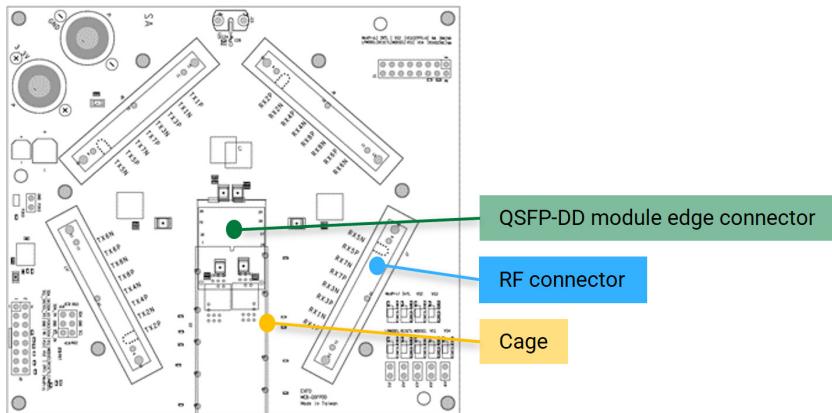
#### **To connect the low speed signal pin and ground:**

Attach the jumper. LED turns Off.

Jumper

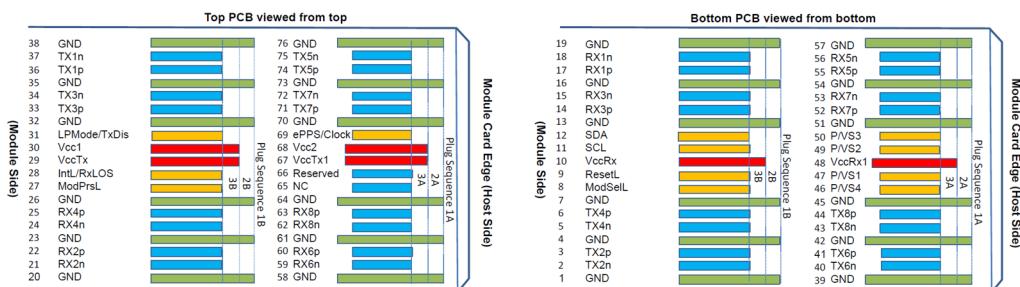


## Module Edge Connector and Cage



- QSFP-DD module edge connector (no warranty: limited insertion life)
- Connector durability is 100 cycles (typical)
- Brand: Yamaichi
- RF connector type: O-SMPM
- Cage brand: Amphenol (w/ heat sink)

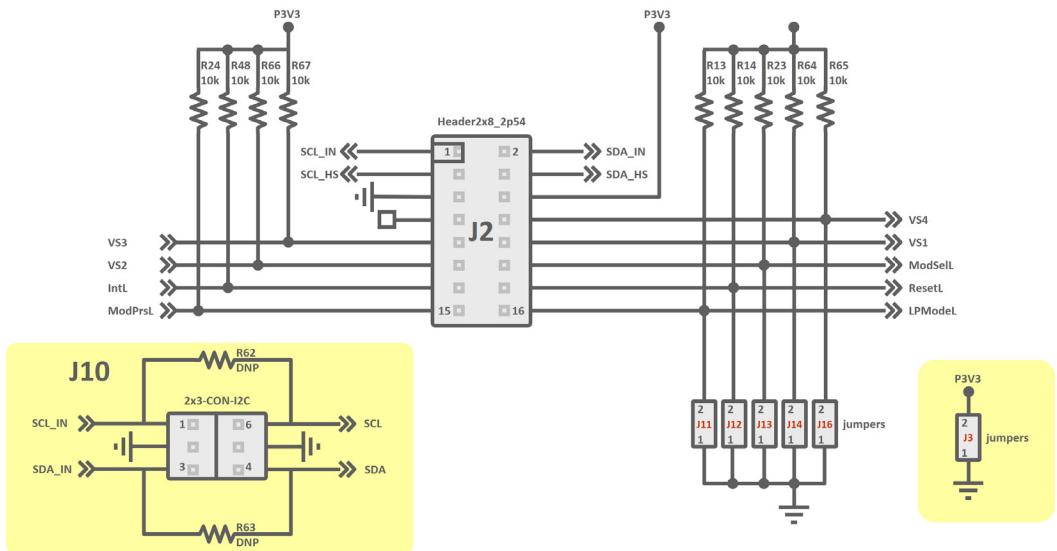
## Module Pad Layout

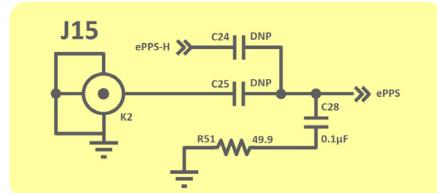
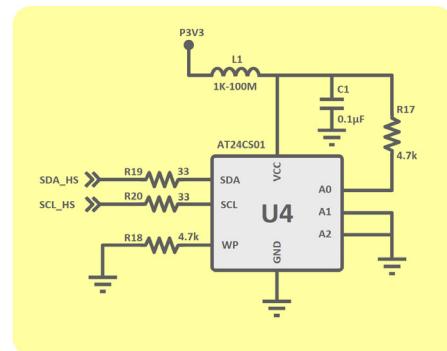
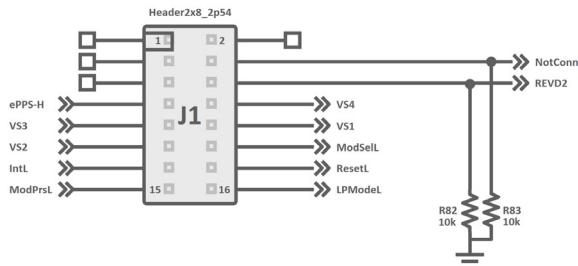


(Ref. QSFP-DD Hardware Rev 6.01)

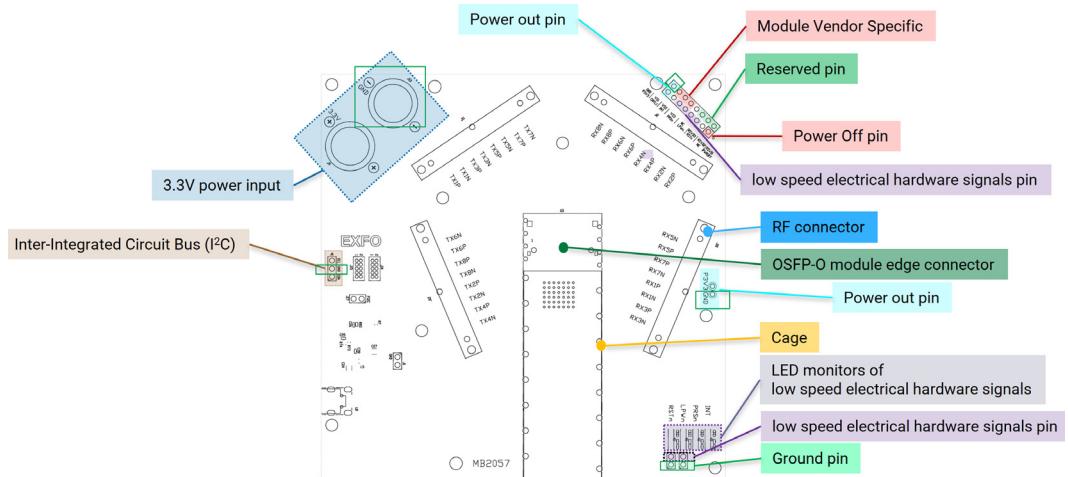
# Schematic of QSFP-DD MCB (O-SMPM)

**Rev. D2 (1/2)**

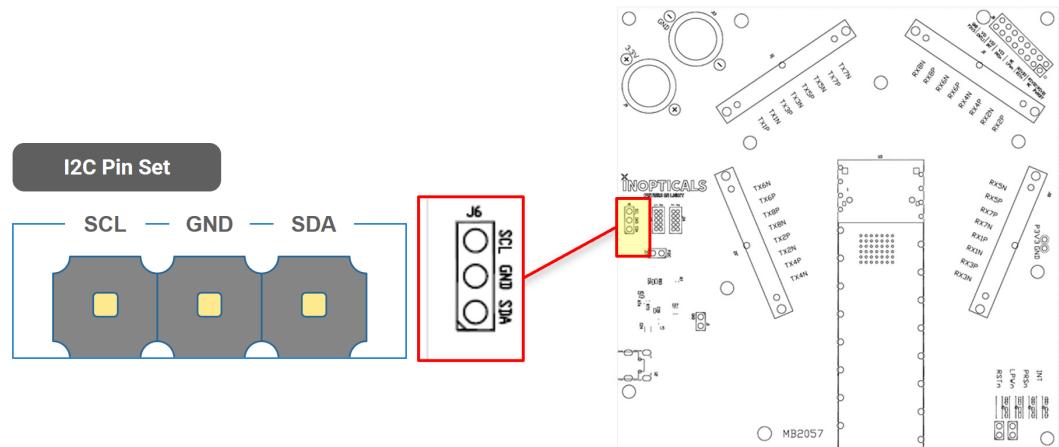


**Rev. D2 (2/2)**

# 3 OSFP-O



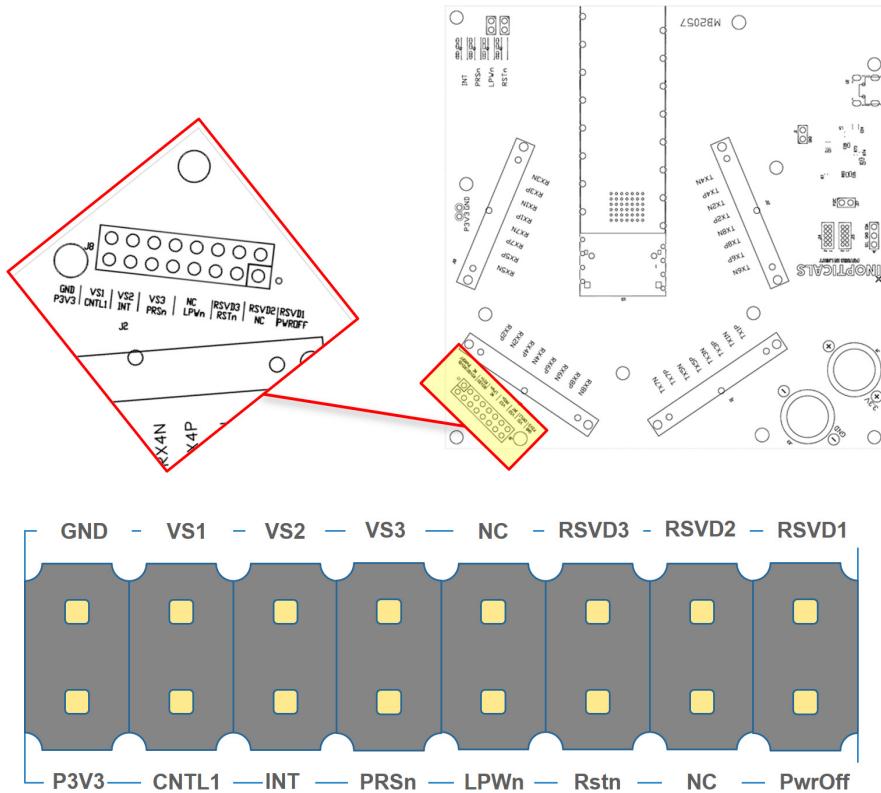
## I<sup>2</sup>C Pin Set Configuration



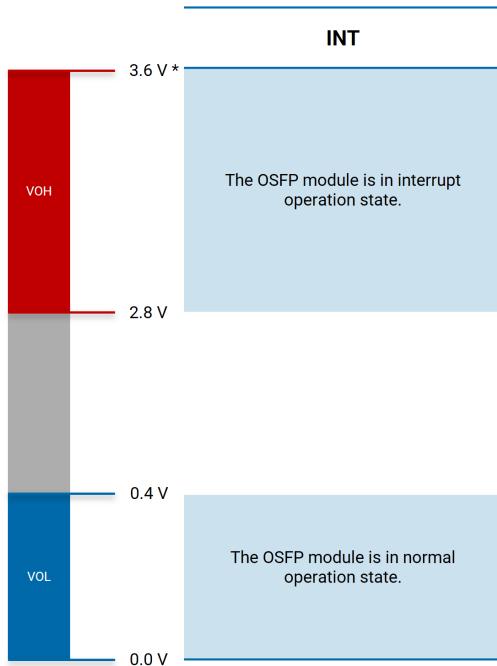
## OSFP-O

Pin

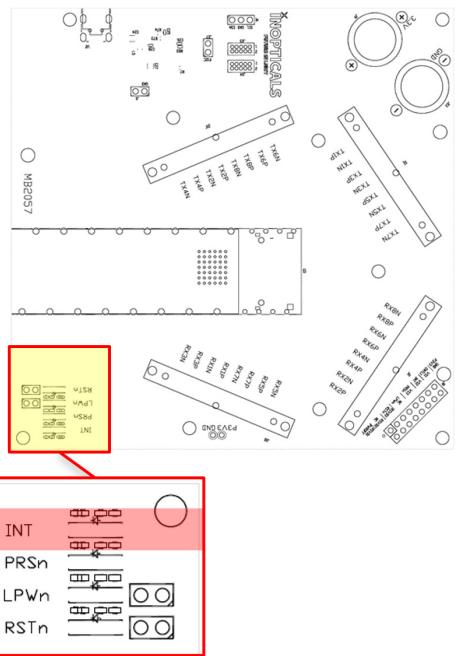
### Pin

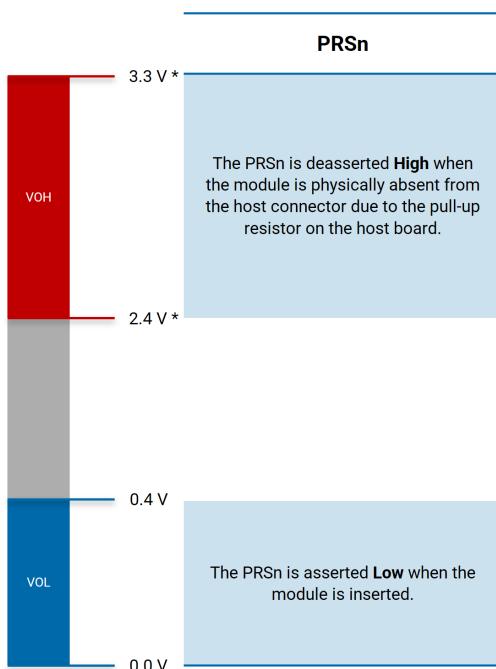


## INT Pin

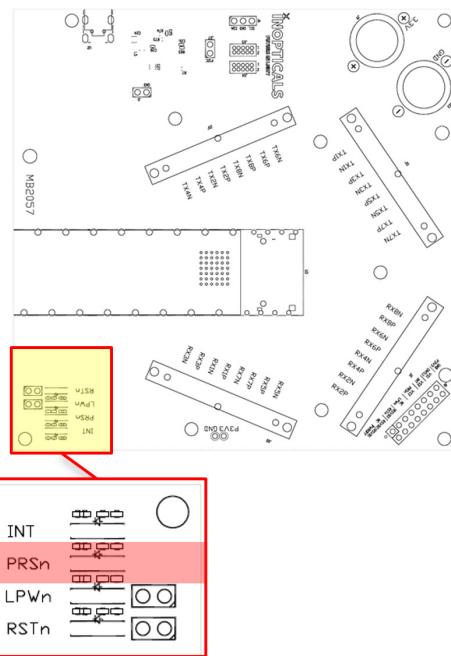


\*Note : Based on the reference value of Vcc as 3.3 V

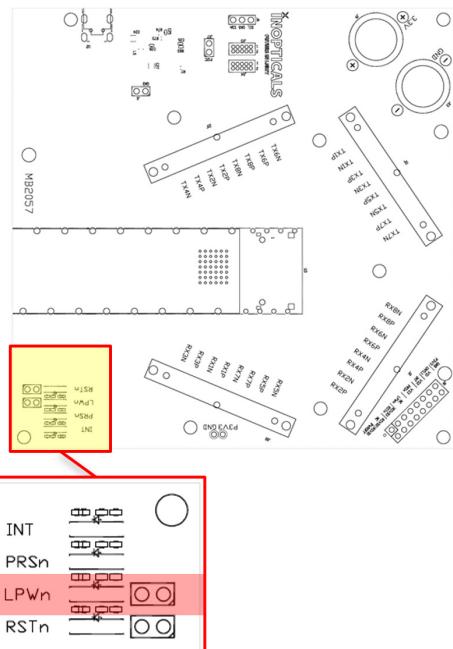
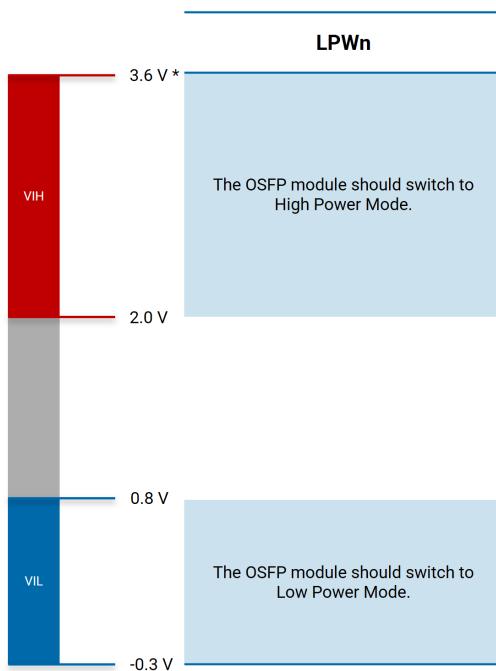
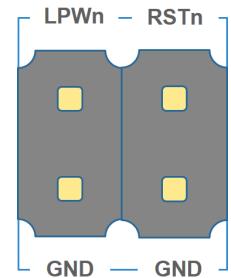


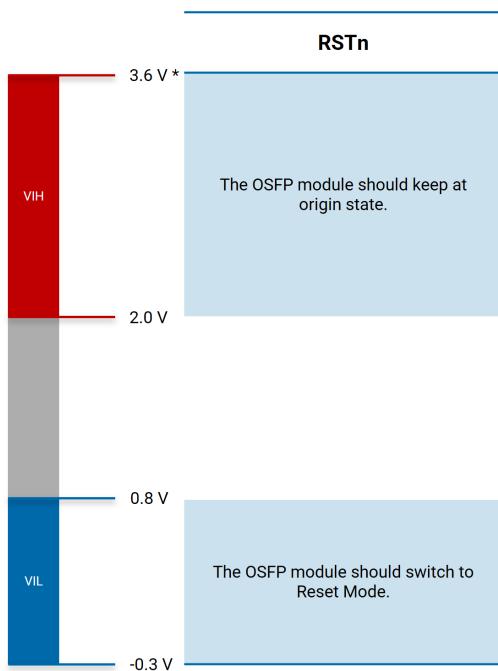
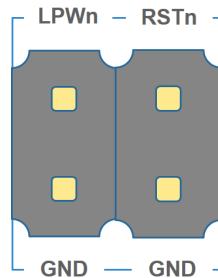
**PRSn Pin**

\*Note : Based on the reference value of Vcc as 3.3 V

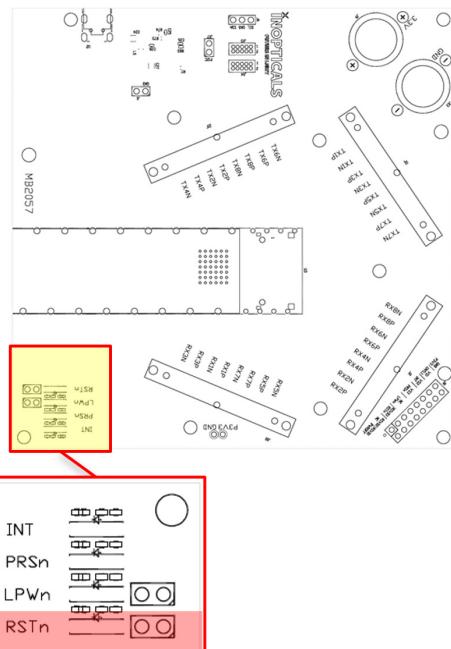


## LPWn Pin

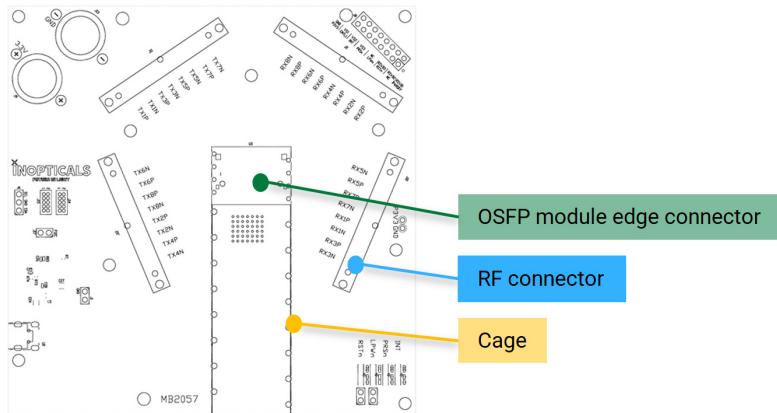


**RSTn Pin**

\*Note : Based on the reference value of Vcc as 3.3 V



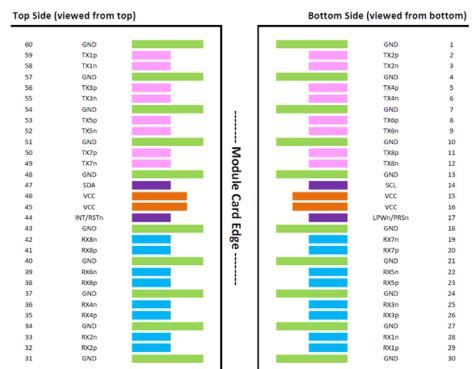
## Module Edge Connector and Cage



- OSFP module edge connector (no warranty: limited insertion life)
- Connector durability is 100 cycles (typical)
- Brand: Yamaichi
- Cage brand: Amphenol (w/ heat sink)
- RF connector type: O-SMPM

## Module Pad Layout

(Ref. OSFP Module Specification  
Rev 3.0)

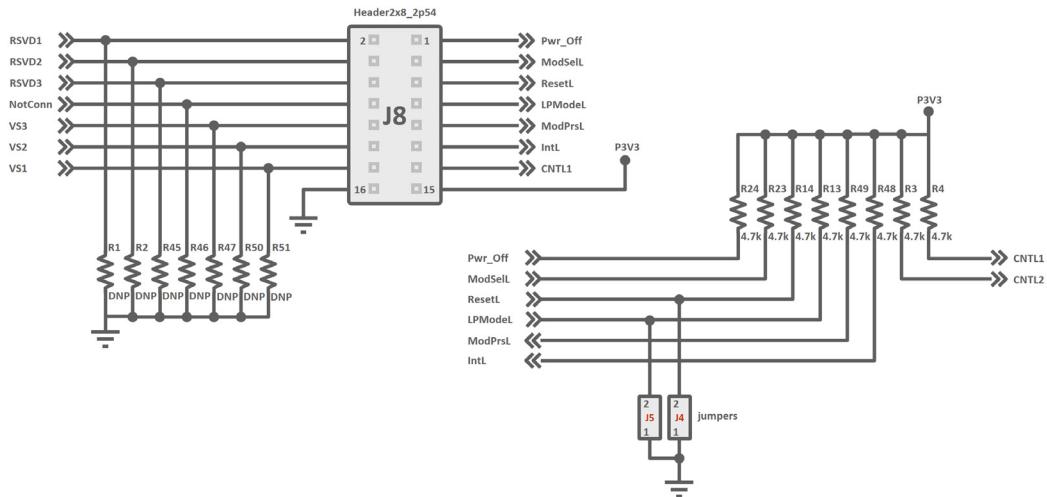


## OSFP-O

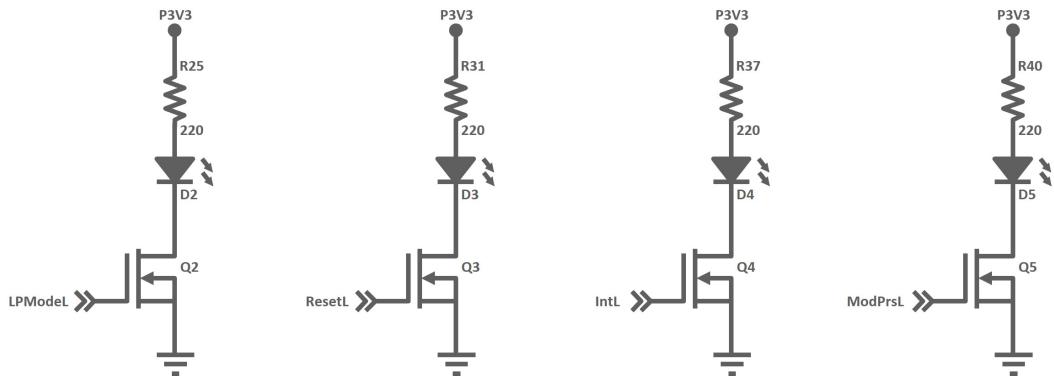
Schematic of OSFP MCB (O-SMPM)

# Schematic of OSFP MCB (O-SMPM)

Rev. D (1/2)

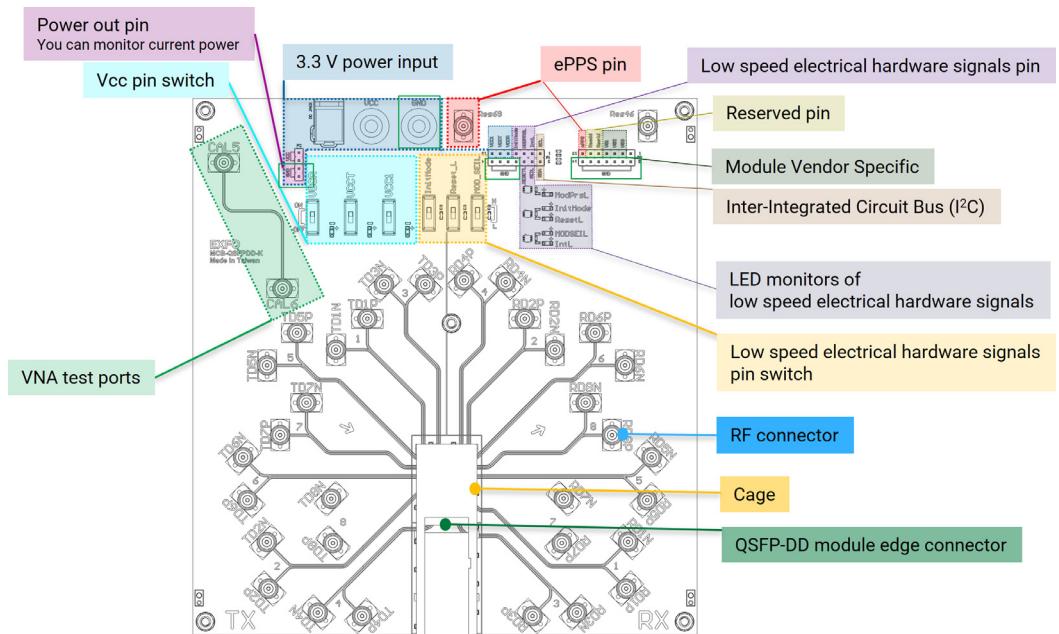


## Rev. D (2/2)

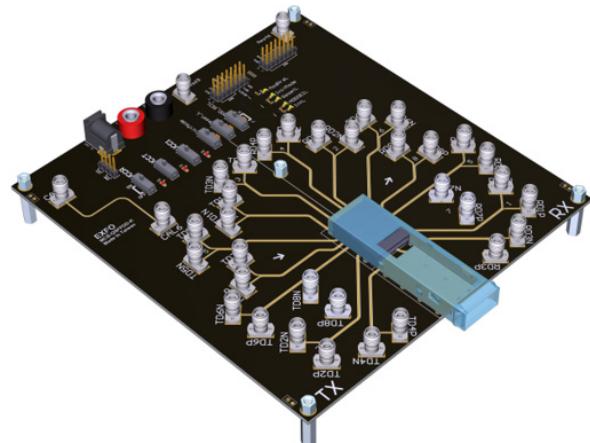




# 4 QSFPDD-K



## Introduction



## Features

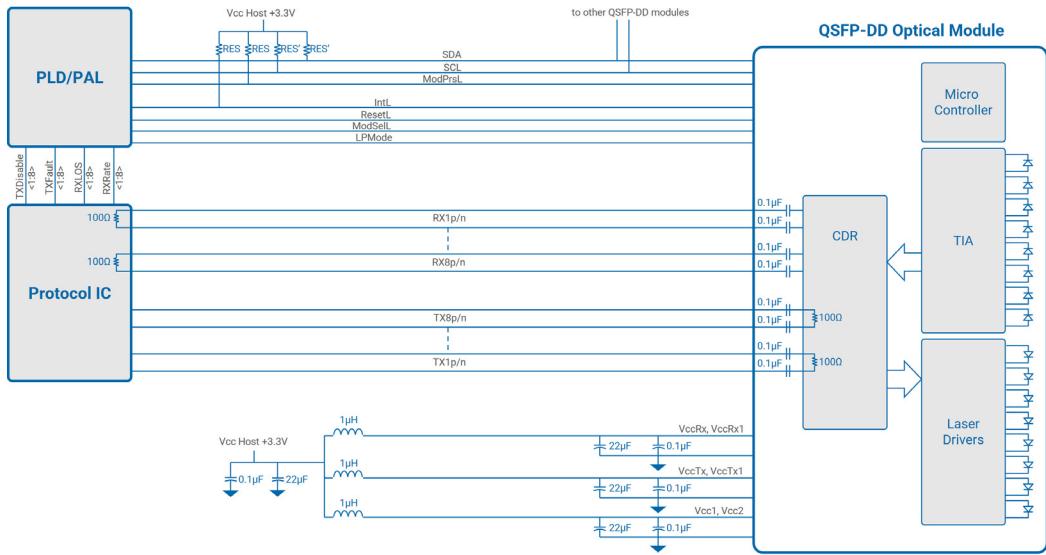
- Supported transceiver:
  - 200G QSFP-DD
  - 400G QSFP-DD
- Operation input voltage: 3.1 to 3.5 V (typically 3.3 V)
- Operation temperature: -40 to +85 °C
- Board dimensions<sup>1</sup>: 170 x 160 mm<sup>2</sup> (w/o cage and hexagonal copper stud)
- RF connector: K (2.92 mm)
- Connector durability: 100 cycles (typical)<sup>2</sup>
- Supported features are as follows:
  - VNA test ports
  - Inter-Integrated Circuit Bus (I<sup>2</sup>C)
  - Standard MSA low speed electrical hardware signals
  - 3 types of Vcc input

---

1. For a drawing file, contact EXFO at support@exfo.com.

2. No warranty – insertion life limited.

# Reference Schematic



**Note:** Filter capacitor values are informative and vary depending on applications. 0.1 $\mu$ F capacitors should be placed in close proximity to power pins and may be duplicated for individual pins to provide additional high frequency filtering.

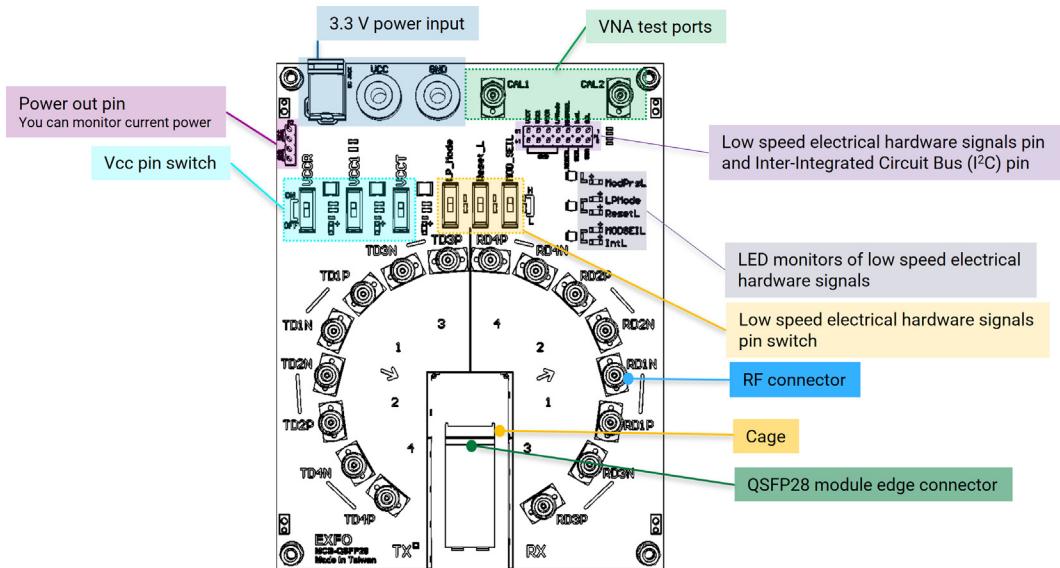
**Note:** Vcc1 and/or Vcc2 may be connected to VccTx, VccTx1 or VccRx, VccRx1 provided the applicable derating of the max current limit is used.

**Note:** RES is a 4.7k to 10k $\Omega$  resistor; RES' is a 1.0k to 3.3 $\Omega$  resistor.

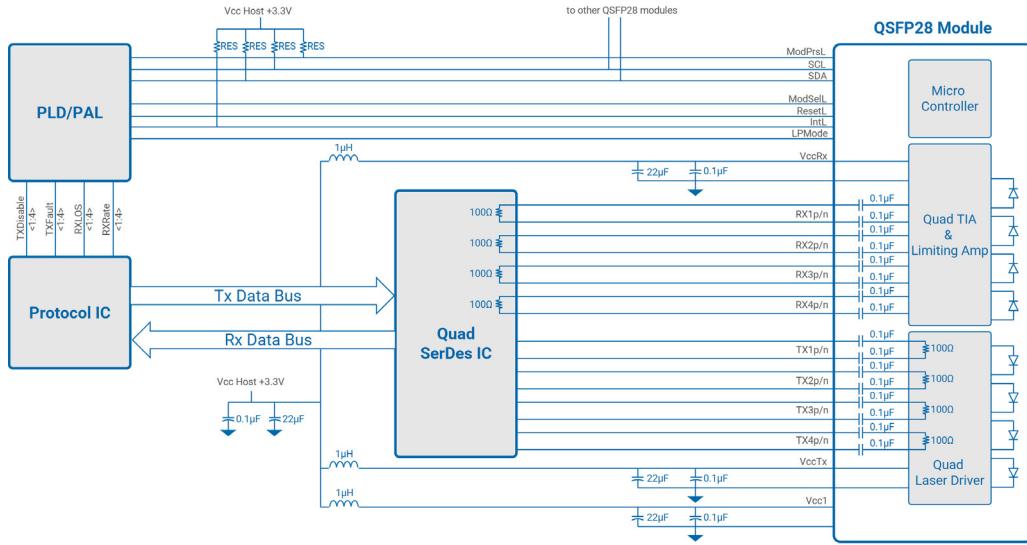


# 5 QSFP28-K

## Hardware Diagram



## Reference Schematic



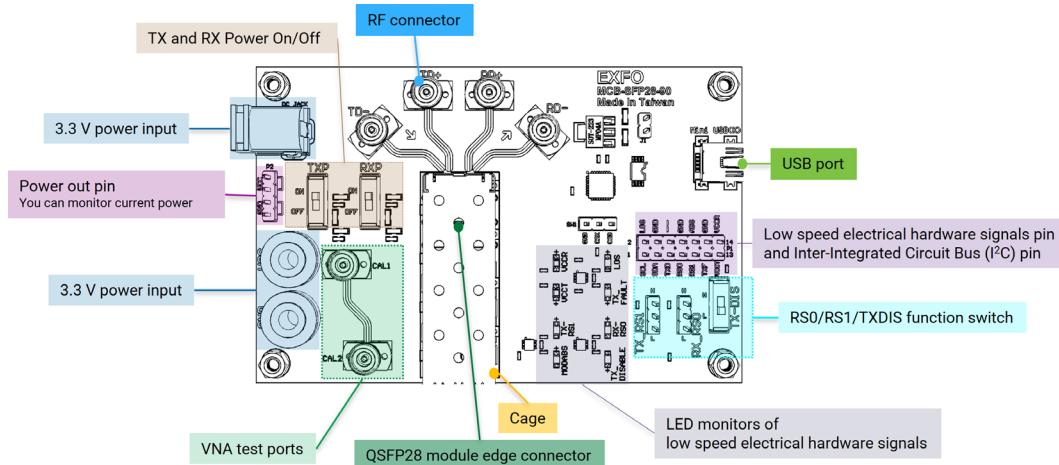
**Note:** Decoupling capacitor values are informative and vary depending on applications.

**Note:** Vcc1 connection may be connected to VccTx or VccRx provided the applicable derating of the max current limit is used.

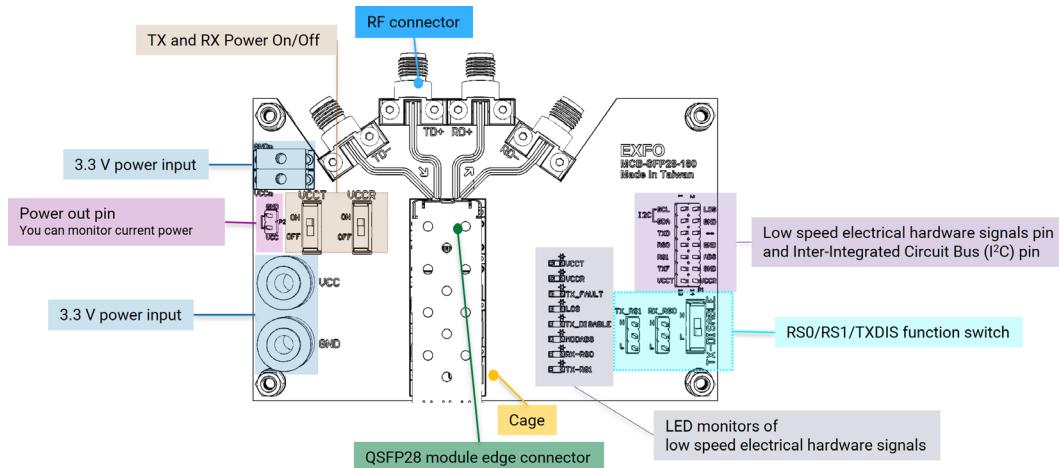
**Note:** RES is a  $4.7k$  to  $10k\Omega$  resistor.

# 6 SFP28-K

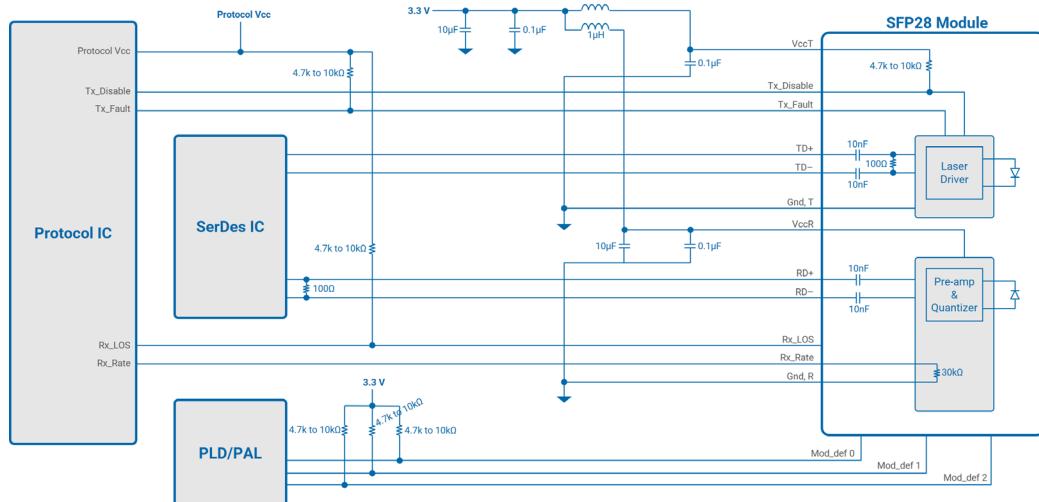
## SFP28-K-90



## SFP28-K-180



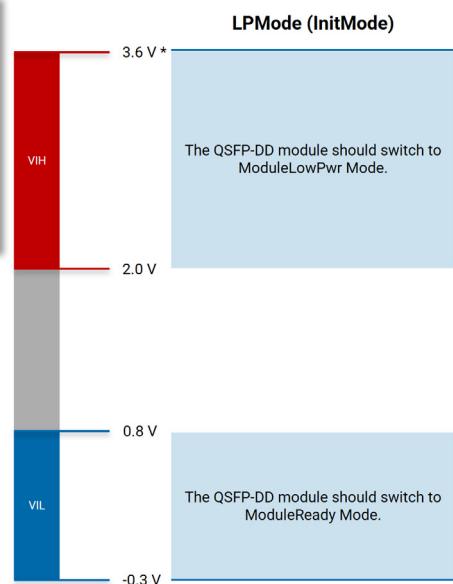
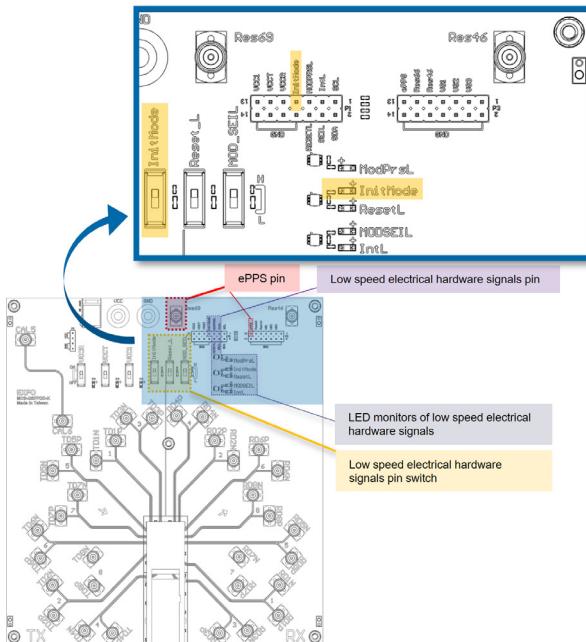
## Reference Schematic



# Low Speed Electrical Hardware Signals

## QSFPDD-K Examples

### LPMode (InitMode)

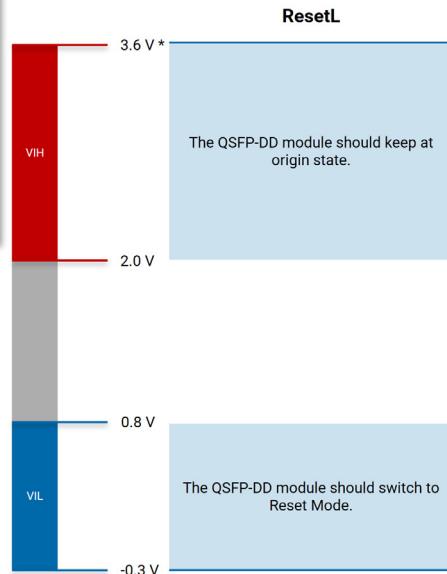
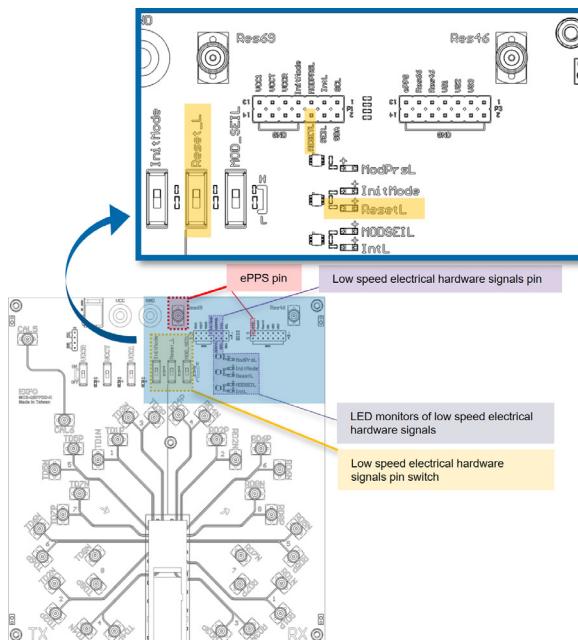


\*Note : Based on the reference value of Vcc as 3.3 V

## Low Speed Electrical Hardware Signals

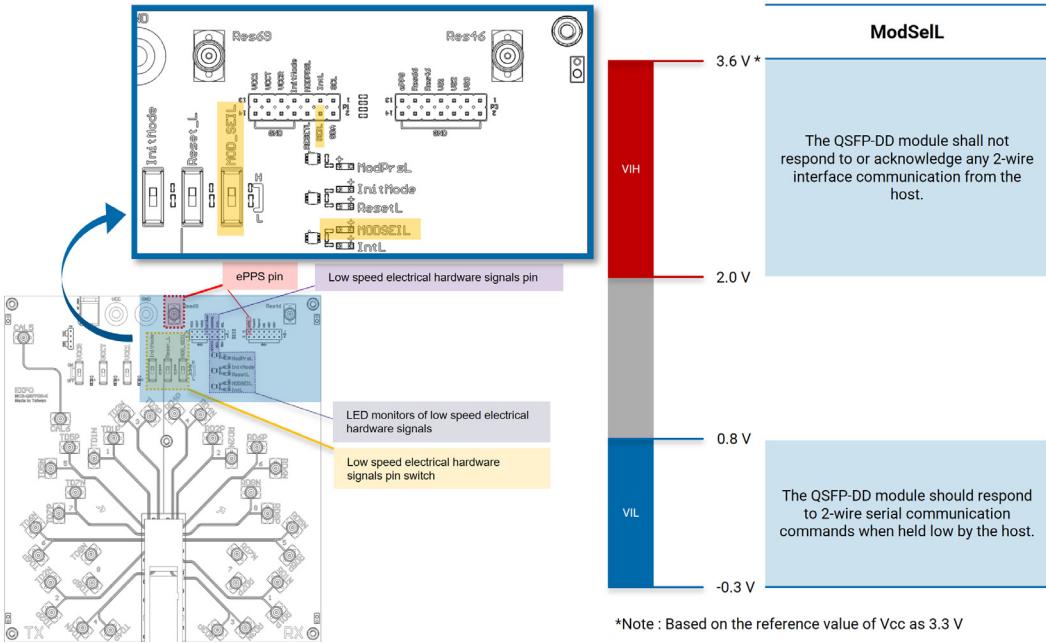
### QSFPPD-K Examples

#### ResetL



\*Note : Based on the reference value of Vcc as 3.3 V

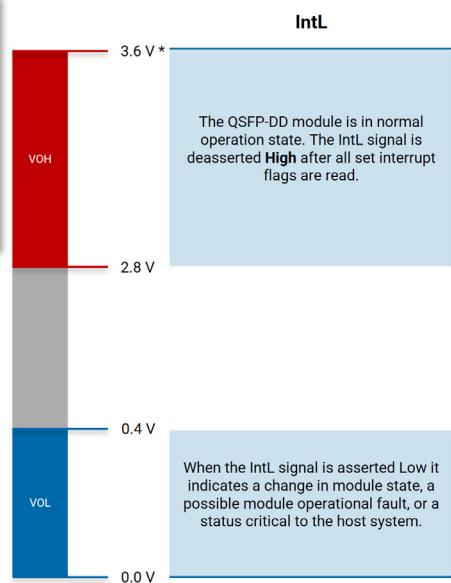
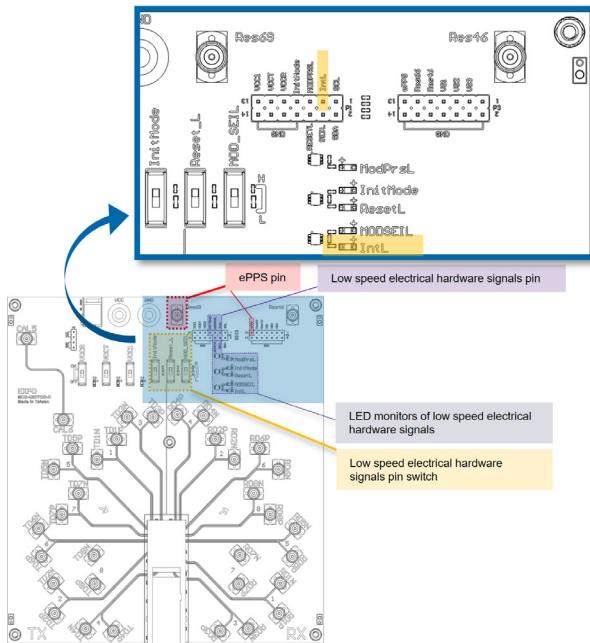
## ModSell



## Low Speed Electrical Hardware Signals

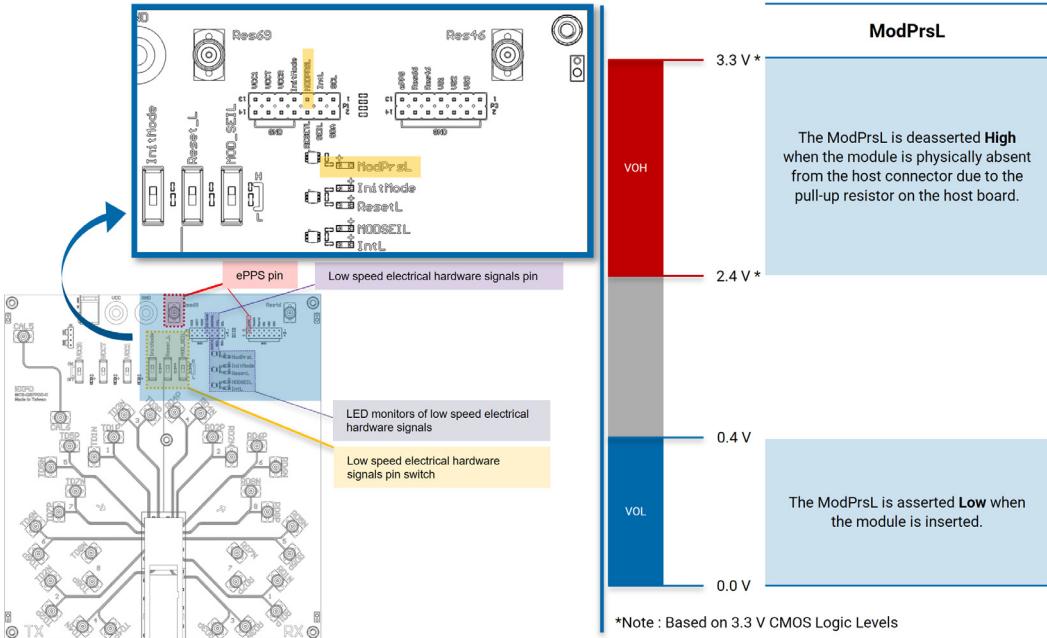
### QSFPDD-K Examples

#### IntL



\*Note : Based on the reference value of Vcc as 3.3 V

### ModPrsL

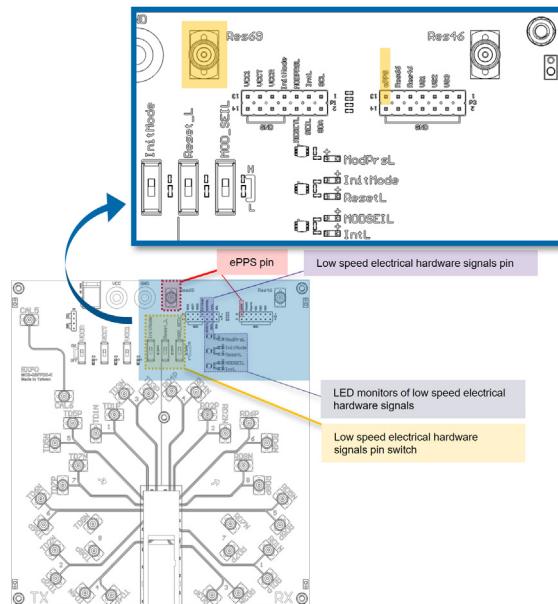


## ePPS (Enhanced Pulse Per Second)

For high-performance Precision Time Protocol (PTP) applications, a PTP reference cl °Ck with Pulse Per Second modulation, ePPS may be provided from the host to the module.

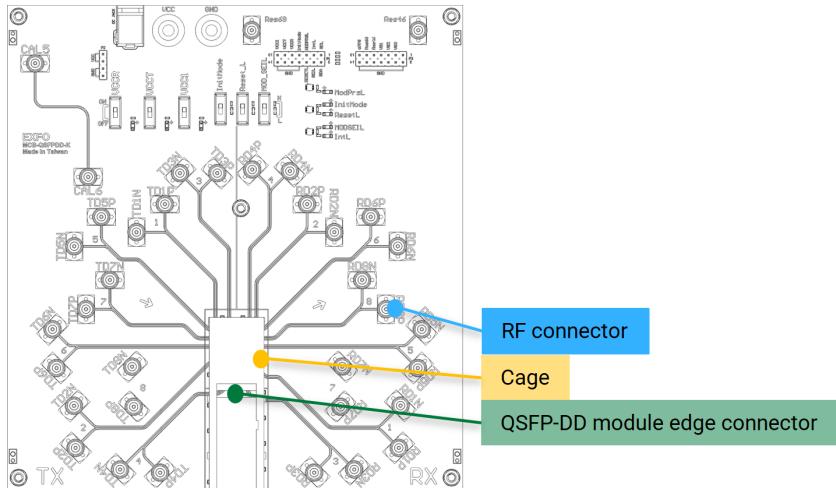
This can be used for either offline delay characterization or real-time delay compensation within the module.

The cl °Ck is used to tightly synchronize the Host Time-of-Day counter to the module's internal Time-of-Day Counter.



**Note:** ePPS is only implemented on QSFPDD.

## Module Edge Connector and Cage

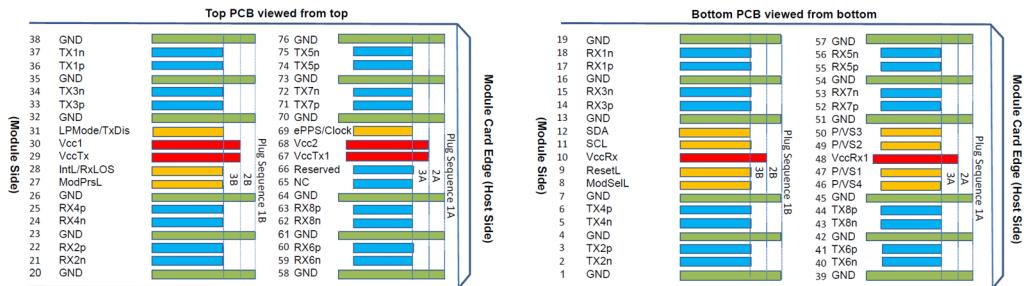


- QSFP-DD module edge connector (no warranty: limited insertion life)
  - Connector durability is 100 cycles (typical)
  - Brand: Yamaichi
- Cage brand: Amphenol (w/ heat sink)
- RF connector:
  - Type: K (2.92 mm)
  - The nominal impedance of differential ports is  $100 \Omega$

# Low Speed Electrical Hardware Signals

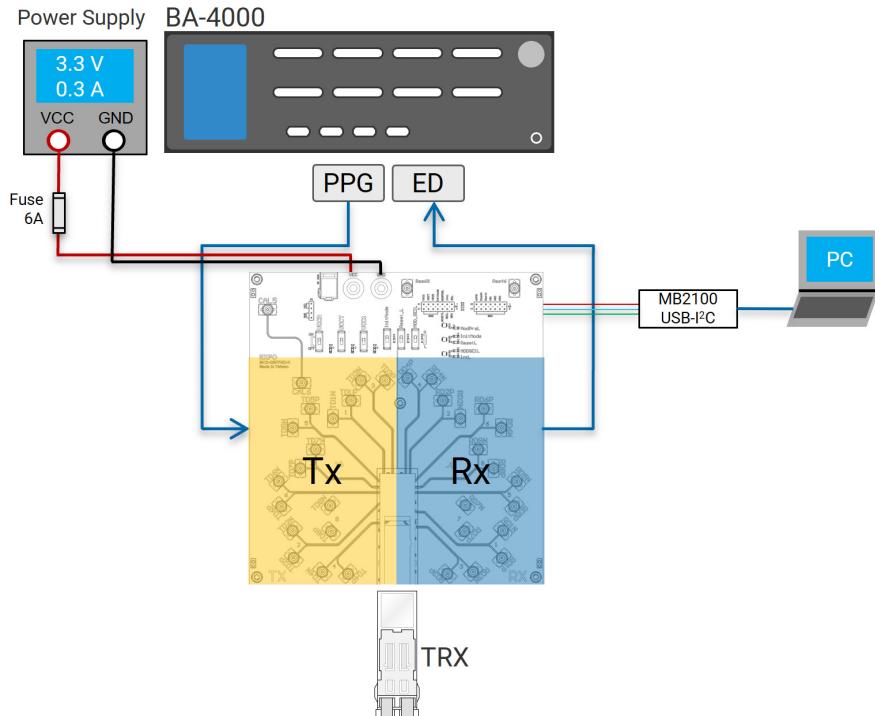
## Module Edge Connector and Cage

### Module Pad Layout



(Ref. QSFP-DD Hardware Rev 6.01)

## Low Power Mode



### For low power mode:

1. LPMode pin set High.
2. ResetL pin set High.
3. ModSelL pin set Low.
4. Power on Power Supply.
5. Set voltage 3.3 V and max. current.<sup>1</sup>

**Note:** It is recommended to use external max. current slow-acting fuse for safety.

1. Ref by QSFP-DD CMIS 4.0

## QSFPDD-K Examples

### Low Power Mode

---

6. Check ModPrsL LED is On (High Voltage).
7. Plug-in QSFP-DD transceiver.
8. Check ModPrsL LED is Off (Low Voltage).
9. Read Byte 0x03 Bits 1-3 should be 001b.

#### 8.2.1 ID and Status

The ID and Status fields described in Table 8-2 provide fundamental memory map characteristics (module type, flat or paged memory, memory map version) as well as module status indicators.

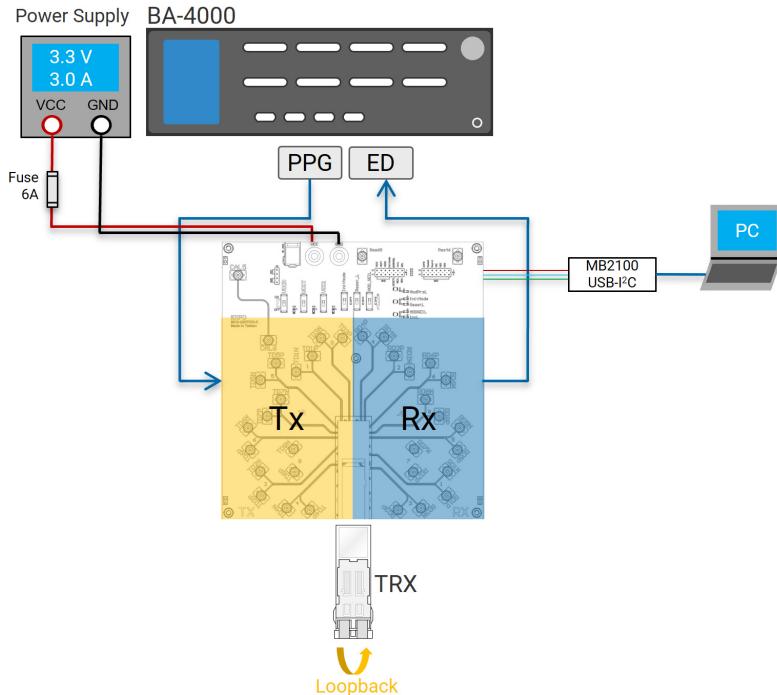
Table 8-2 Identifier and Status Summary (Lower Page)

Byte	Bits	Field Name	Description	Type
0	7-0	Identifier	Identifier - Type of Serial Module - See SFF-8024.	RO RQD
1	7-0	Revision Compliance	Identifier – CMIS revision; the upper nibble is the whole number part and the lower nibble is the decimal part. 00h=CMIS 2.1, 21h indicates version 2.1.	RO RQD
2	7	Flat_mem	Upper memory flat or paged. 0b=Paged memory (pages 00h, 01h, 02h, 10h and 11h are implemented) 1b=Flat memory (only page 00h implemented)	RO RQD
6	Reserved			
5-4	Reserved			
3-2	TWI Maximum speed		Indicates maximum two-wire serial speed supported by module. 00b=Module supports up to 400 kHz 01b=Module supports up to 1 MHz 10b=Module supports up to 10 MHz 11b=Reserved	RO RQD
1-0	Reserved			
3	4-1	Module state	Current state of Module (see Table 8-3)	RO
0	1	Interrupt	Update state of interrupt output signal 0b=Interrupt asserted 1b=Interrupt not asserted (default)	RQD

Table 8-3 Module State Encodings

Code	Module state
0000	Module power state
0010	ModulePowerOn state
0100	ModulePowerOff state
0110	ModuleReady state (reported by flat memory modules)
1000	ModuleFault state
1010	Fault state
1100	Reserved
1110	Reserved

## High Power Mode



### **For high power mode:**

1. LPMode pin set Low.
2. ResetL pin set High.
3. ModSelL pin set Low.
4. Power on Power Supply.
5. Set voltage 3.3 V and max. current.<sup>1</sup>

**Note:** It is recommended to use external max. current slow-acting fuse for safety.

1. Ref by QSFP-DD CMIS 4.0

## QSFPDD-K Examples

### High Power Mode

6. Check ModPrsL LED is On (High Voltage).
7. Plug-in QSFP-DD transceiver.
8. Check ModPrsL LED is Off (Low Voltage).
9. Read Byte 0x03 Bits 1-3 should be 011b.
10. Loopback QSFP-DD transceiver.
11. Set BA-4000 amplitude and other parameters.
12. Read BER value from BA-4000.

#### 8.2.1 ID and Status

The ID and Status fields described in Table 8-2 provide fundamental memory map characteristics (module type, flat or paged memory, memory map version) as well as module status indicators.

Table 8-2 Identifier and Status Summary (Lower Page)

Byte	Bits	Field Name	Description	Type
0	7-0	Identifier	Identifier - Type of Serial Module - See SFF-8024. 0b=RQD 1b=RQO	RQD RQO
1	7-0	Revision Compliance	Identifier – CMIS revision; the upper nibble is the whole number part and the lower nibble is the decimal part. 00b=CMIS version 1.0 01b=CMIS version 1.1, 21b indicates version 2.1.	RQD RQO
2	7	Flat_mem	Upper memory flat or paged. 0b=Paged memory (pages 00h, 01h, 02h, 10h and 11h are implemented) 1b=Flat memory (only page 00h implemented)	RQD RQO
6	Reserved			
5-4	Reserved			
3-2	TWI Maximum speed		Indicates maximum two-wire serial speed supported by module. 00b=Module supports up to 400 kHz 01b=Module supports up to 1 MHz 10b=Module supports up to 10 MHz 11b=Reserved	RQD RQO
1-0	Reserved			
3	4-3	Module state	Current state of Module (see Table 8-3)	RQD RQO
3	3-1	Module state		
0	Interrupt		Digital state of interrupt output signal 0b=Interrupt asserted 1b=Interrupt not asserted (default)	RQD RQO

Table 8-3 Module State Encodings

Code	Module state
000b	Power off state
001b	Module low power state
010b	Module booting state
011b	Module ready state (reported by flat memory modules)
100b	Module fault state
101b	Fault state
110b	Reserved
111b	Reserved

# Index

## B

BER ..... 1, 40

## C

capacitor ..... 23, 26

connector

    durability ..... 8, 17, 22, 35

    type ..... 8, 17, 35

control tool ..... 4

## F

fuse ..... 37, 39

## H

hardware signals ..... 1, 22

## I

impedance ..... 35

## J

jumpers ..... 4

## L

LEDs ..... 7, 38, 40

## P

power pins ..... 23

PTP ..... 34

## R

resistor ..... 23, 26

## T

temperature ..... 22

test ports ..... 1, 22

transceiver

    loopback ..... 40

    plug-in ..... 40

    supported ..... 22

## V

voltage ..... 23, 26, 37, 38, 39, 40

    input ..... 1, 22

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